



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

501.39484X00

#5
Electron
10/8/02
as

Applicants: YAMADA et al.
Serial No.: 09/767,830
Filed: January 24, 2001
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND PROCESS FOR MANUFACTURING THE SAME
Group: 2815
Examiner: G. Eckert II

RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner
for Patents
Washington, D.C. 20231

October 3, 2002

Sir:

In response to the restriction requirement dated September 5, 2002,
applicants hereby elect the Group I, claims 1-35, drawn to making a device, without
traverse.

If the Examiner believes that there are any other points which may be clarified
or otherwise disposed of, either by telephone discussion or by personal interview,
the Examiner is invited to contact applicants' undersigned attorney at the number
indicated below.

It is respectfully requested that any shortage in the fee be charged to the
account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (Case No.
501.39484X00).

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

By

Gregory E. Montone
Registration No. 28,141

GEM/kd
703/312-6600

TECHNOLOGY CENTER 2800

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